

CPU-Z Ver. 1.90.0.x64

CPU | Caches | Mainboard | **Memory** | SPD | Graphics | Bench | About

General

Type **DDR3** Channel # **Dual**  
 Size **16 GBytes** DC Mode  
 NB Frequency **2139.7 MHz**

Timings

DRAM Frequency	668.7 MHz
FSB:DRAM	2:10
CAS# Latency (CL)	8.0 docks
RAS# to CAS# Delay (tRCD)	8 docks
RAS# Precharge (tRP)	8 docks
Cycle Time (tRAS)	24 docks
Row Refresh Cycle Time (tRFC)	174 docks
Command Rate (CR)	1T
DRAM Idle Timer	
Total CAS# (tRDRAM)	
Row To Column (tRCD)	

Tools | Validate | Close

CPU-Z Ver. 1.90.0.x64

CPU | Caches | Mainboard | **Memory** | SPD | Graphics | Bench | About

General

Type **DDR3** Channel # **Dual**  
 Size **20 GBytes** DC Mode  
 NB Frequency **2137.4 MHz**

Timings

DRAM Frequency	667.9 MHz
FSB:DRAM	2:10
CAS# Latency (CL)	9.0 docks
RAS# to CAS# Delay (tRCD)	9 docks
RAS# Precharge (tRP)	9 docks
Cycle Time (tRAS)	24 docks
Row Refresh Cycle Time (tRFC)	174 docks
Command Rate (CR)	2T
DRAM Idle Timer	
Total CAS# (tRDRAM)	
Row To Column (tRCD)	

Tools | Validate | Close

CPU-Z Ver. 1.90.0.x64

CPU | Caches | Mainboard | Memory | **SPD** | Graphics | Bench | About

Memory Slot Selection

**Slot #1** | **DDR3** | Module Size **8 GBytes**  
 Max Bandwidth **PC3-12800H (800 MHz)** | SPD Ext. **XMP 1.3**  
 Module Manuf. **Crucial Technology** | Week/Year  
 DRAM Manuf. | Ranks **Dual**  
 Part Number **BLS8G3D1609DS1S00.** | Correction  
 Serial Number **8917004F** | Registered

Timings Table

	JEDEC #5	JEDEC #6	JEDEC #7	XMP-1600
Frequency	800 MHz	800 MHz	800 MHz	800 MHz
CAS# Latency	9.0	10.0	11.0	9.0
RAS# to CAS#	9	9	9	9
RAS# Precharge	9	9	9	9
tRAS	24	24	24	24
tRC	39	39	39	37
Command Rate				2T
Voltage	1.50 V	1.50 V	1.50 V	1.500 V

Tools | Validate | Close

CPU-Z Ver. 1.90.0.x64

CPU | Caches | Mainboard | Memory | **SPD** | Graphics | Bench | About

Memory Slot Selection

**Slot #3** | **DDR3** | Module Size **8 GBytes**  
 Max Bandwidth **PC3-12800H (800 MHz)** | SPD Ext. **XMP 1.3**  
 Module Manuf. **Crucial Technology** | Week/Year  
 DRAM Manuf. | Ranks **Dual**  
 Part Number **BLS8G3D1609DS1S00.** | Correction  
 Serial Number **891700BA** | Registered

Timings Table

	JEDEC #5	JEDEC #6	JEDEC #7	XMP-1600
Frequency	800 MHz	800 MHz	800 MHz	800 MHz
CAS# Latency	9.0	10.0	11.0	9.0
RAS# to CAS#	9	9	9	9
RAS# Precharge	9	9	9	9
tRAS	24	24	24	24
tRC	39	39	39	37
Command Rate				2T
Voltage	1.50 V	1.50 V	1.50 V	1.500 V

Tools | Validate | Close

CPU-Z Ver. 1.90.0.x64

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Memory Slot Selection

**Slot #2** | **DDR3** | Module Size **2 GBytes**  
 Max Bandwidth **PC3-10700 (667 MHz)** | SPD Ext.  
 Module Manuf. **Kingston** | Week/Year **10 / 10**  
 DRAM Manuf. | Ranks **Dual**  
 Part Number **9905458-012.A00LF** | Correction  
 Serial Number **EA1C94BA** | Registered

Timings Table

	JEDEC #1	JEDEC #2	JEDEC #3	JEDEC #4
Frequency	457 MHz	533 MHz	609 MHz	666 MHz
CAS# Latency	6.0	7.0	8.0	9.0
RAS# to CAS#	6	7	8	9
RAS# Precharge	6	7	8	9
tRAS	17	20	22	24
tRC	23	27	30	33
Command Rate				
Voltage	1.50 V	1.50 V	1.50 V	1.50 V

Tools | Validate | Close

CPU-Z Ver. 1.90.0.x64

CPU | Caches | Mainboard | Memory | **SPD** | Graphics | Bench | About

Memory Slot Selection

**Slot #4** | **DDR3** | Module Size **2 GBytes**  
 Max Bandwidth **PC3-10700 (667 MHz)** | SPD Ext.  
 Module Manuf. **Kingston** | Week/Year **10 / 10**  
 DRAM Manuf. | Ranks **Dual**  
 Part Number **9905458-012.A00LF** | Correction  
 Serial Number **EB1C90BA** | Registered

Timings Table

	JEDEC #1	JEDEC #2	JEDEC #3	JEDEC #4
Frequency	457 MHz	533 MHz	609 MHz	666 MHz
CAS# Latency	6.0	7.0	8.0	9.0
RAS# to CAS#	6	7	8	9
RAS# Precharge	6	7	8	9
tRAS	17	20	22	24
tRC	23	27	30	33
Command Rate				
Voltage	1.50 V	1.50 V	1.50 V	1.50 V

Tools | Validate | Close